

FIG. 1A

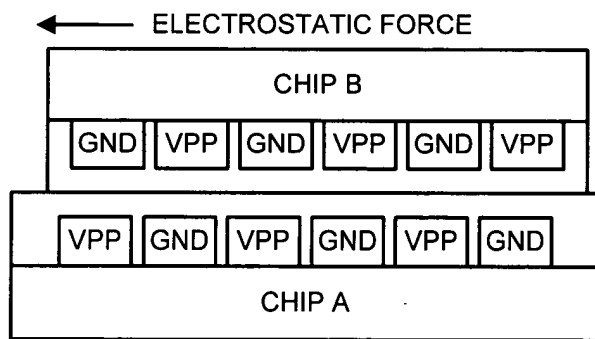


FIG. 1B

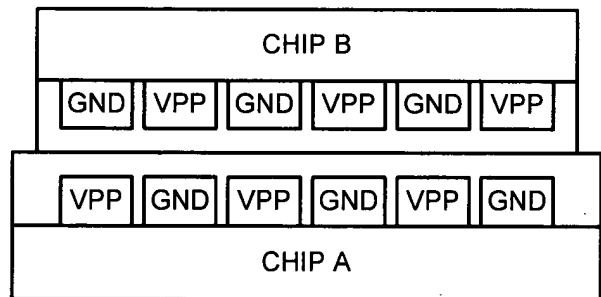


FIG. 1C

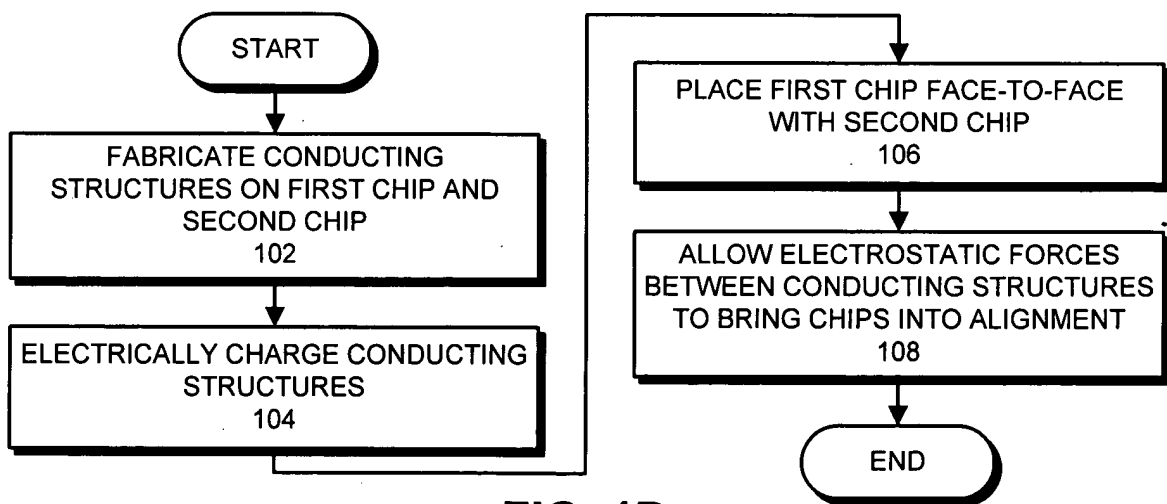


FIG. 1D



FIG. 2

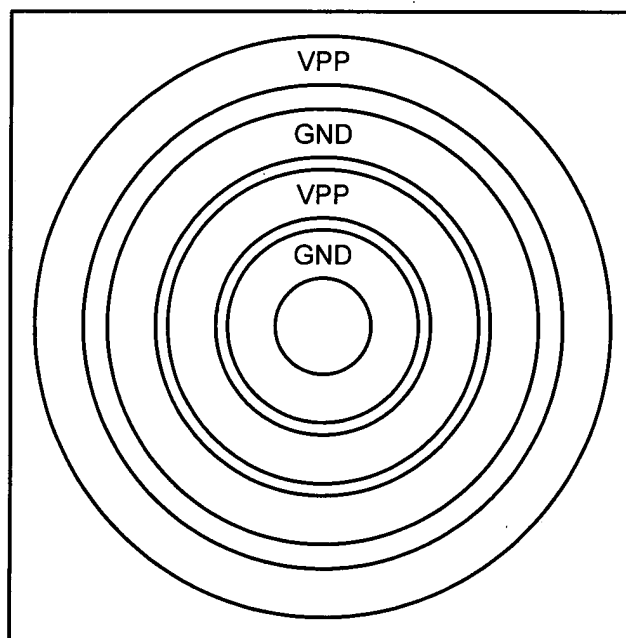


FIG. 3

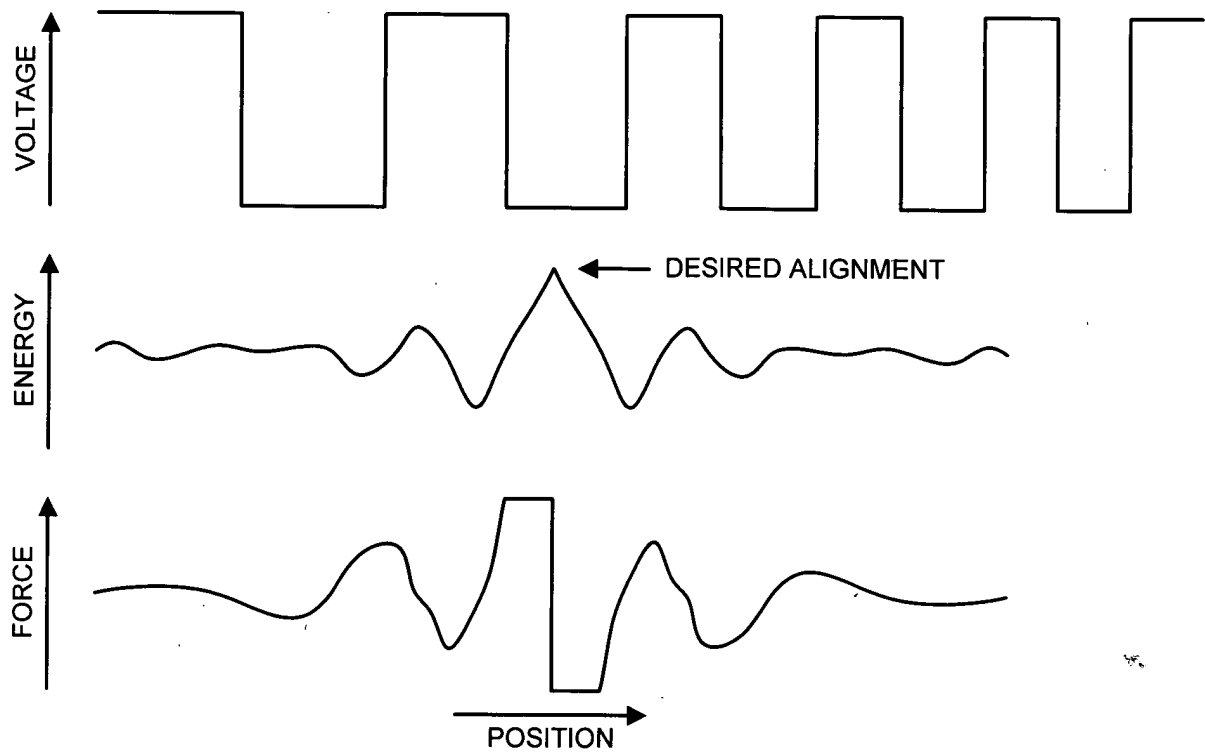


FIG. 4

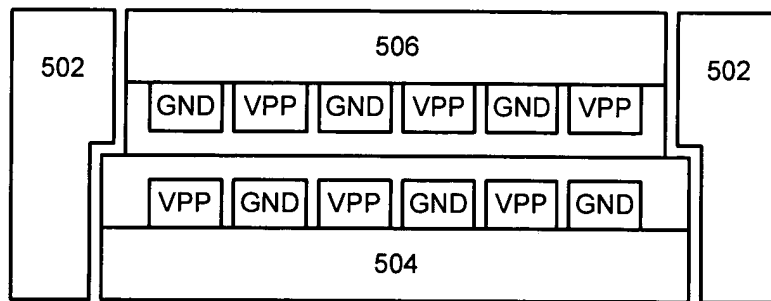
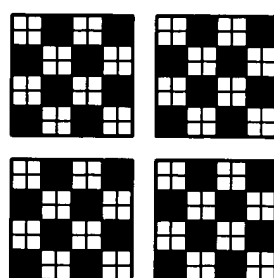


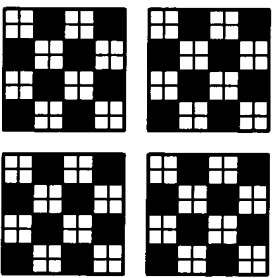
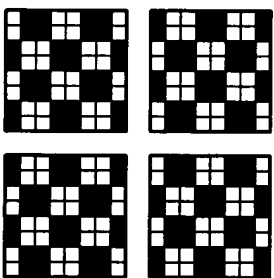
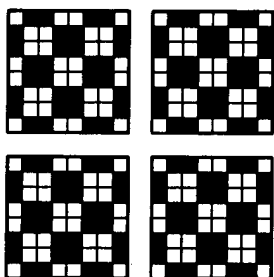
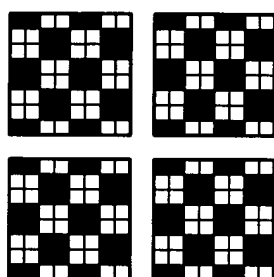


FIG. 5

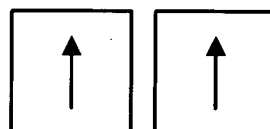
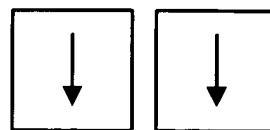
CHIP B



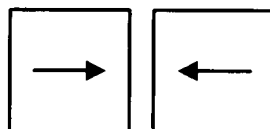
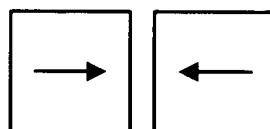
 VPP SQUARE
 GND SQUARE



SHIFT VECTORS



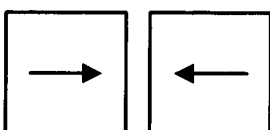
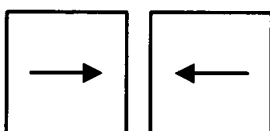
STATE2



STATE3



STATE4



STATE1

FIG. 6

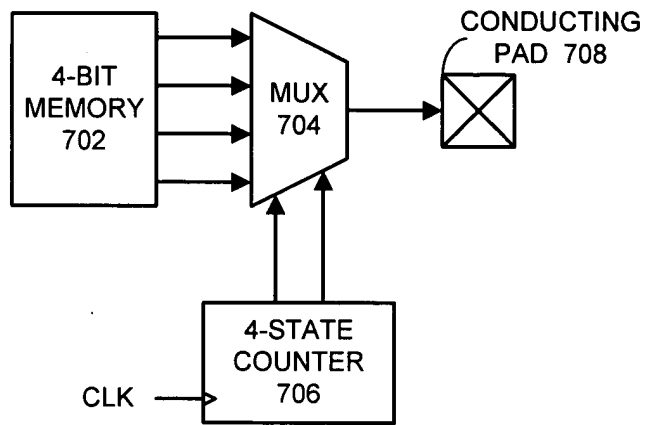


FIG. 7